

## Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

## Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

## Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Search Results [PDF FULL-TEXT 792 KB] PREV NEXT DOWNLOAD CITATION



## COTS for military applications: been there, done that, got the PC cards

Balakirsky, J. Hall, W.J., Jr.

Orbital Sci. Corp., Germantown, MD, USA ;

*This paper appears in: Nonvolatile Memory Technology Conference, 1996., Sixth Biennial IEEE International*

Meeting Date: 06/24/1996 - 06/26/1996

Publication Date: 24-26 June 1996

Location: Albuquerque, NM USA

On page(s): 16 - 23

Reference Cited: 7

Inspec Accession Number: 5436330

### Abstract:

OSC/FD has successfully implemented unmodified PC Cards in military equipment. These implementations have been for memory only. In one instance PC Cards are being used in a new system (Bradley Fighting Vehicle Mass Memory Unit) and in another instance PC Cards are being used to cost effectively **upgrade** an existing system (the PC Card **upgrade** to the AN/ASQ-215 Navy standard data loader). OSC/FD is working on the application of PC Cards (or Credit Electronics) beyond memory. Both **retrofit** and new developments are being considered. Using PC Cards, with their limited processing and interface capabilities, in a complex, multi-processor military applications is significant challenge, but one OSC/FD will achieve

### Index Terms:

add-on boards military equipment smart cards COTS OSC/FD PC cards interface capabilities military applications military equipment multi-processor military applications **retrofit** system **upgrade**

### Documents that cite this document

There are no citing documents available in IEEE Xplore at this time.

Search Results [PDF FULL-TEXT 792 KB] PREV NEXT DOWNLOAD CITATION

## Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

## Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

## Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Search Results [PDF FULL-TEXT 1176 KB] PREV NEXT DOWNLOAD CITATION



## Embedded PC barriers and solutions: new solutions for creating long-life 386- and 486-class embedded PCs

Reed, B.

Component Products, RadiSys Corp., Beaverton, OR , USA;

*This paper appears in: Northcon/96*

Meeting Date: 11/04/1996 - 11/06/1996

Publication Date: 4-6 Nov. 1996

Location: Seattle, WA USA

On page(s): 191 - 201

Reference Cited: 0

Number of Pages: viii+444

Inspec Accession Number: 5526956

**Abstract:**

This presentation explores the challenges we face as **embedded** designers when trying to design a long life 386- or 486-class **embedded PC**. Several key issues are identified and solutions are evaluated. In particular, **embedded** PCs based upon the Intel386 EX and Intel486 families of processors and the new RadiSys chipsets are explored. The challenges of ensuring component availability over the long-term is discussed and alternatives are reviewed. Benefits from implementing an **embedded PC** are shown to be real and several examples of successful Intel386 EX and Intel486 based implementations are discussed

**Index Terms:**

development systems microcomputers real-time systems Intel386 EX processor Intel486 processor RadiSys chipset component availability **embedded PC** design life-cycle

**Documents that cite this document**

There are no citing documents available in IEEE Xplore at this time.

Search Results [PDF FULL-TEXT 1176 KB] PREV NEXT DOWNLOAD CITATION